

**PATENT**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:  
WARREN M. FARNWORTH  
KEVIN G. DUESMAN

Serial No.: 10/822,113

Confirmation No.: 1702

Filed: April 8, 2004

For: NANOTUBE SEMICONDUCTOR DEVICES  
AND METHODS FOR MAKING THE SAME

Patent No. 7,081,385 B2

Issue Date: July 25, 2006

Group Art Unit: 2814

Examiner: Shrinivas H. Rao

Att'y Docket: 2008.011482

Customer No.: 23720

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

The patentee requests a certificate of correction for Patent and Trademark Office ("PTO") errors pursuant to 37 C.F.R. § 1.322. Because this error was the PTO's, no fee should be required. If any fee is required, please charge Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2008.011482.

The following error is listed on the attached form PTO-1050.

Col. 8, line 64 (claim 12, line 2), change "mode" to -- made --.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON  
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Date: August 15, 2006

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UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,081,385 B2  
 DATED : July 25, 2006  
 INVENTOR(S) : Warren M.. Farnworth and Kevin G. Duesman

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 8, line 64 (claim 12, line 2), change "mode" to -- made --.

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PATENT NO. 7,081,385 B2  
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